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**FACSIMILE COVER SHEET**

To Examiner: Steven Loke	Total Pages Sent: 2
Technology Center 2800	(including cover sheet)
Facsimile Number: 703-872-9306	Transmission Date: October 21, 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Lehmann, et al. Docket No.: 00P09113 US  
Serial No: 09/751,474 Art Unit: 2811  
Date Filed: December 28, 2000  
Title: Area Efficient Stacking of Antifuses in Semiconductor Device

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- Certification of Facsimile Transmission (1 page)
- Election (1 page)

Respectfully submitted,



Natalie Swider  
Legal Assistant

**Confirmation Respectfully Requested**

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For: Area Efficient Stacking of Antifuses in Semiconductor Device

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Election**

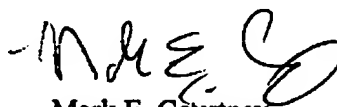
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Dear Sir:

In response to Examiner's Restriction Requirement dated September 28, 2004, Applicants herein elect species (1) depicted in Figs. 1-3; therefore, Applicants elect to prosecute claims 1, 6-10 and 16-20 without traverse.

Please contact Applicants' attorney at the number listed below with any questions regarding this matter.

Respectfully submitted,



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Reg. No. 36,491  
Attorney for Applicants

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